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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	09/854,905
				Filing Date	May 15, 2001
				First Named Inventor	MOORE, Brian
				Group Art Unit	2829
				Examiner Name	
				Attorney Docket Number	11157-23
Sheet	1	of	1		

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Examiner Signature	Jimmy Naughton	Date Considered	6/6/03
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.
³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Attorney Docket Number	11157-23

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner
Signature**

Timmy Nguyen

Date Considered

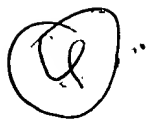
6/4/02

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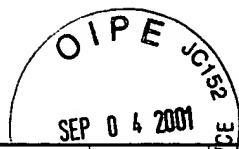
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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. 11157-23	SERIAL NO. 09/854,905
(Use several sheets If necessary)	APPLICANT BRIAN MOORE	
	FILING DATE May 15, 2001	GROUP 2858

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPLICABLE
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f	0911640	04/28/99	Europe	G 01R 29	027	Not needed
v	99/16107	04/01/99	PCT	H01L	--	Not needed
v	99/32893	07/01/99	PCT	G01R 31	00	Not needed
v	99/65287	12/23/99	PCT	N/a	N/a	Not needed

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- Semiconductor Material and Device Characterization – by Dieter K. Schroder – 1990 – pages 1-40
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- Testing the Monster Chip – IEEE Spectrum, (July 1999) – by Yervant Zorian – Pages 54-60

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DATE CONSIDERED

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Examiner Signature	Jimmy Naumen	Date Considered	6/4/03
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